



# **Upgrade Solution using S57-II CPU**

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# S5 System

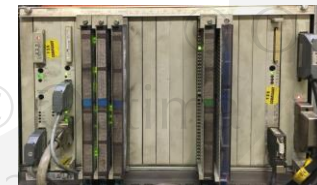
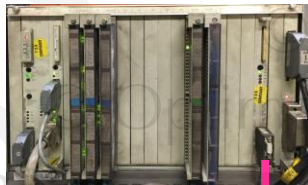
## Status:

- Simatic S5 SPS-System 135U
  - Master Racks CPU is 928B
- Serial or limited comms to the existing PLC system – difficult to upgrade SCADA
- Short timescales available for upgrade route

- SINEC H1
- PROFIBUS
- Ethernet
- PROFINET

## Reason for upgrade:

- Adding new conveyor systems to a system eventually running on a S7-1516-3 PN/DP PLC
- In the interim, these new conveyors need to be controlled from the S5 PLC
- Need to map data across from the S5 to the new conveyors



STEP5



PG/PC  
PLC B



Peripheral  
IO



Peripheral  
IO



Old  
Conveyors



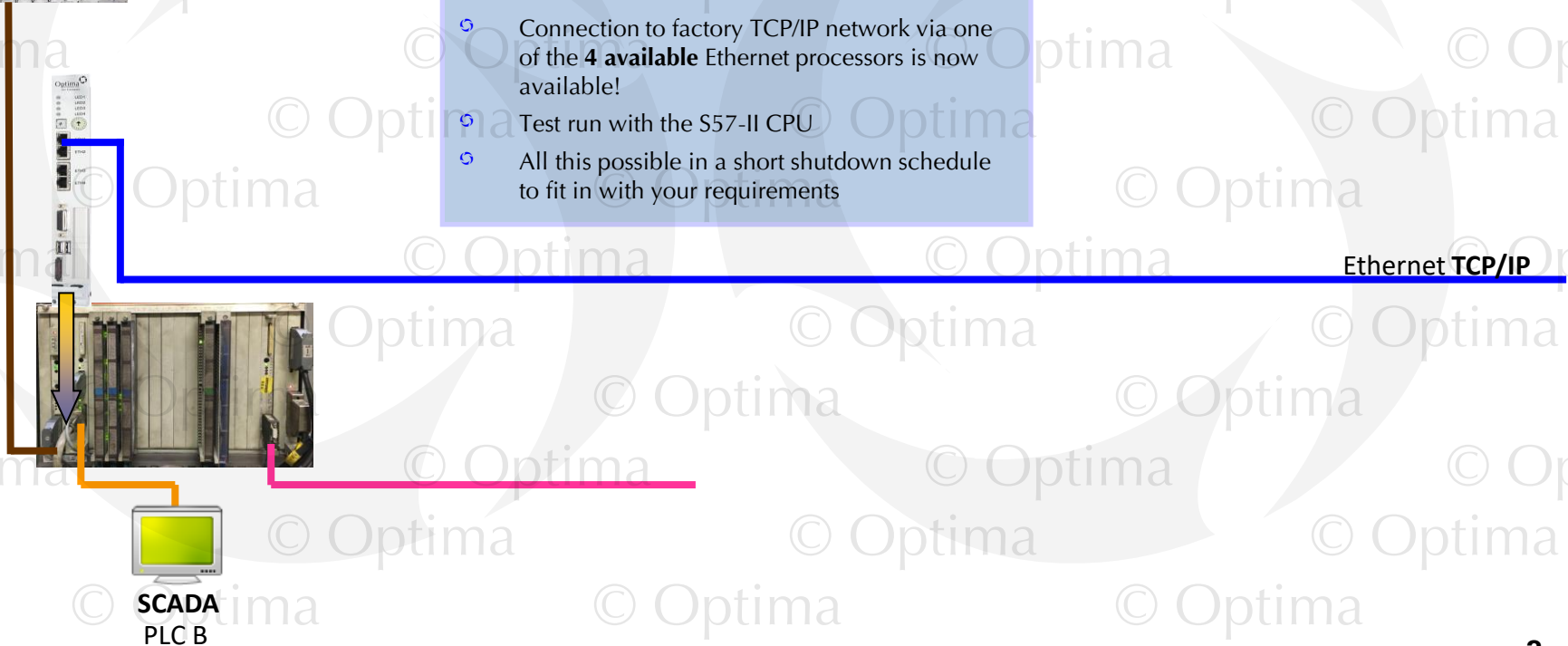
PG/PC  
PLC n

# STEP 1



- ⌚ Program taken from existing processor and installed in the S57-II CPU. Fully tested off-site for program operation
- ⌚ The power to the existing control will be switched off, the CPU928B will be replaced with S57-II CPU. Time < 5 minutes
- ⌚ Connection to factory TCP/IP network via one of the **4 available** Ethernet processors is now available!
- ⌚ Test run with the S57-II CPU
- ⌚ All this possible in a short shutdown schedule to fit in with your requirements

- Serial / AS511
- SINEC H1
- PROFIBUS
- New Ethernet
- PROFINET

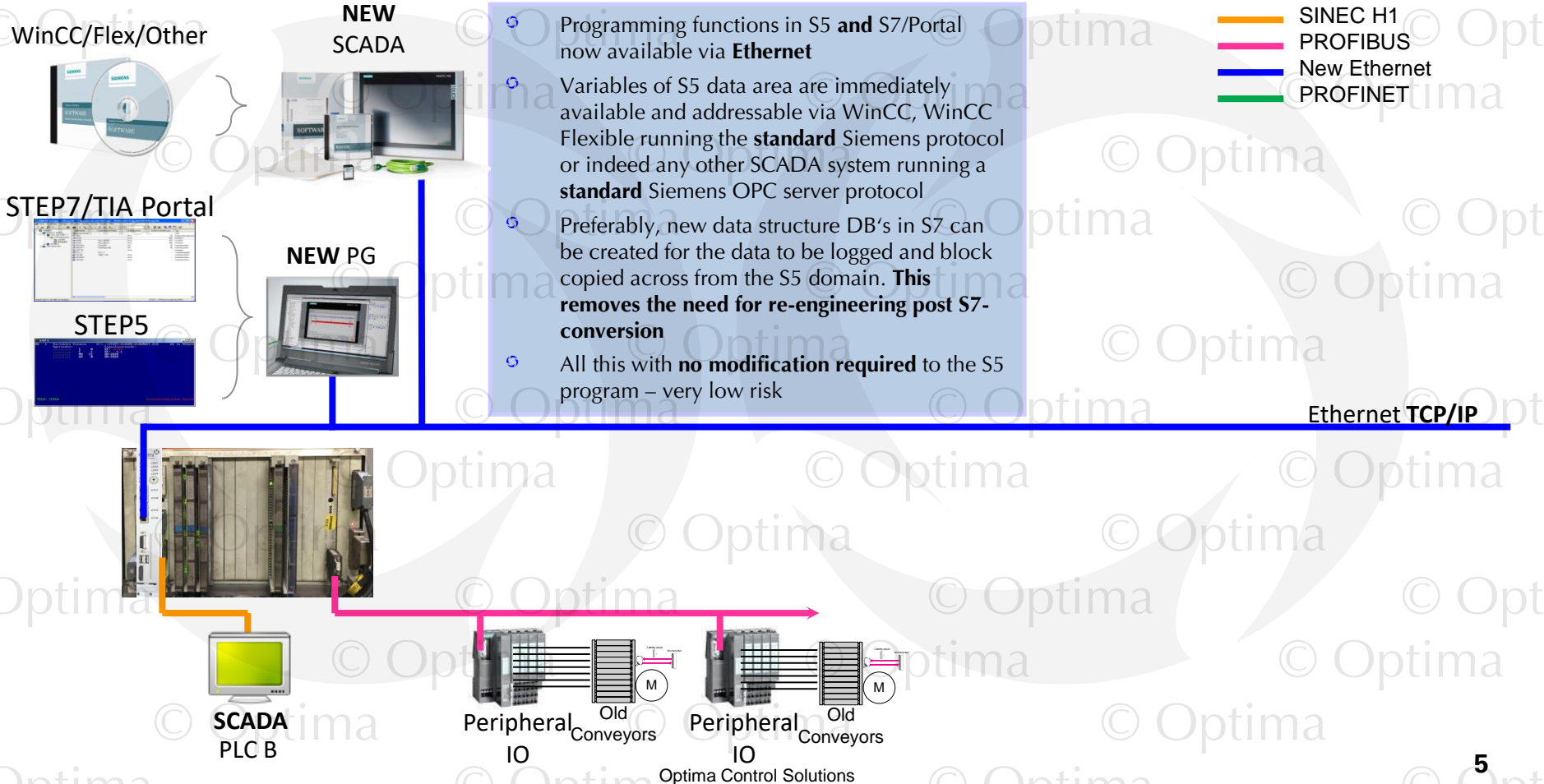


# Immediate Benefits

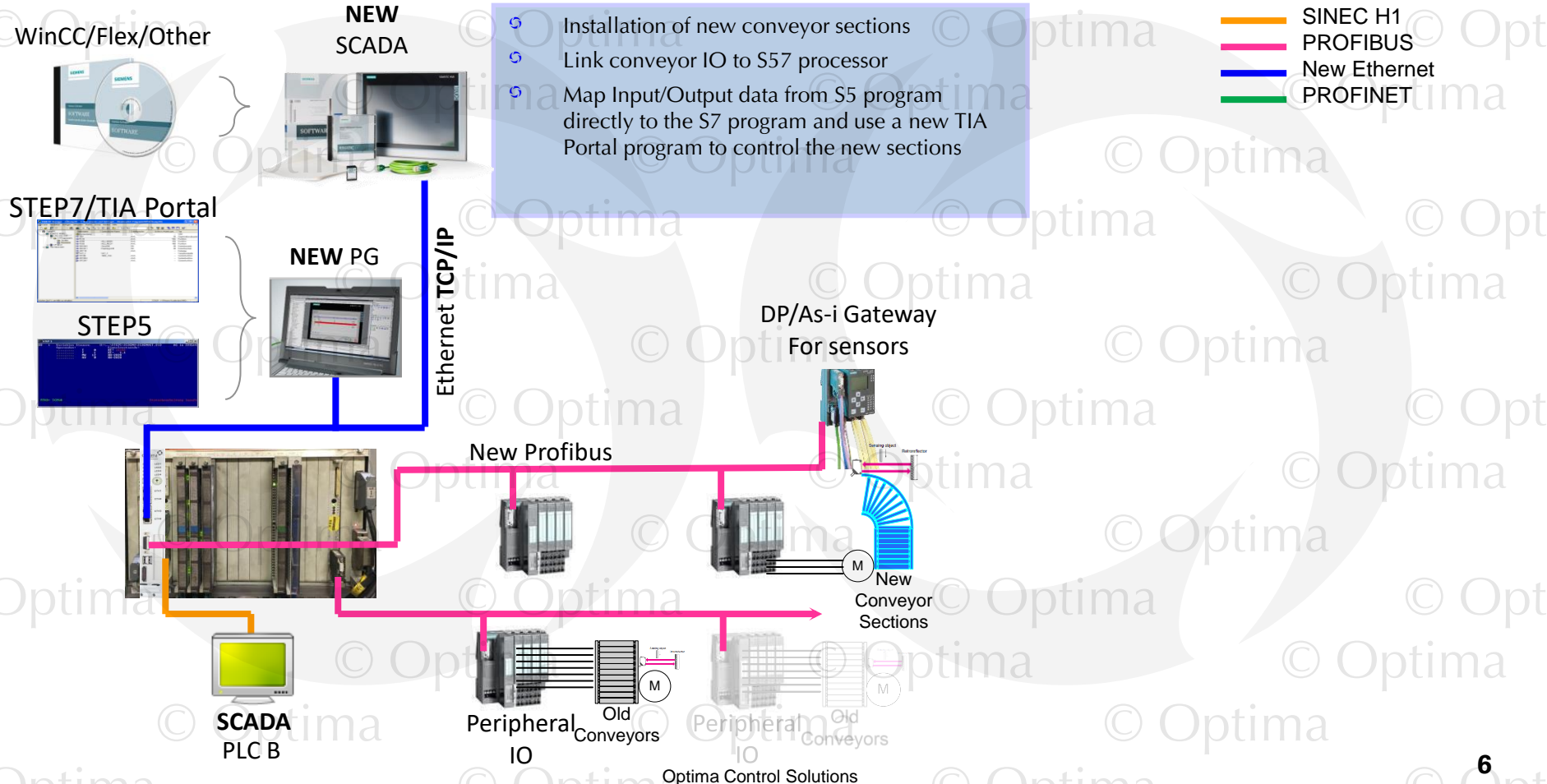
- Short changeover time
- The existing and reliable S5 program is retained with minimal modifications (only those related to the S57-II CPU installation)
- Shorter scan-cycle times (see table below) typically **10x faster** than the highest-specification S5 processor
- 4 Ethernet communication processors available, high-speed access to program data variables is available with no disruption to S5 or S7 scan cycle times
- New SCADA system can be implemented and tested with no disruption to existing SCADA systems or other plant communications

S5 Program operations	S57-II CPU	S5 CPU 948	S5 CPU 945
Bit/Word Operations	10 ns	180 ns	100 ns
Fixed Point Arithmetic	10 ns	500 ns	350 ns
Floating Point Arithmetic	10 ns	3330 ns	1350 ns
S7 Program operations	S57-II CPU	S7 CPU 417	CPU 1516-3 PN/DP
Bit/Word Operations	10 ns	18 ns	10 ns
Fixed Point Arithmetic	10 ns	18 ns	16 ns
Floating Point Arithmetic	10 ns	54 ns	64 ns

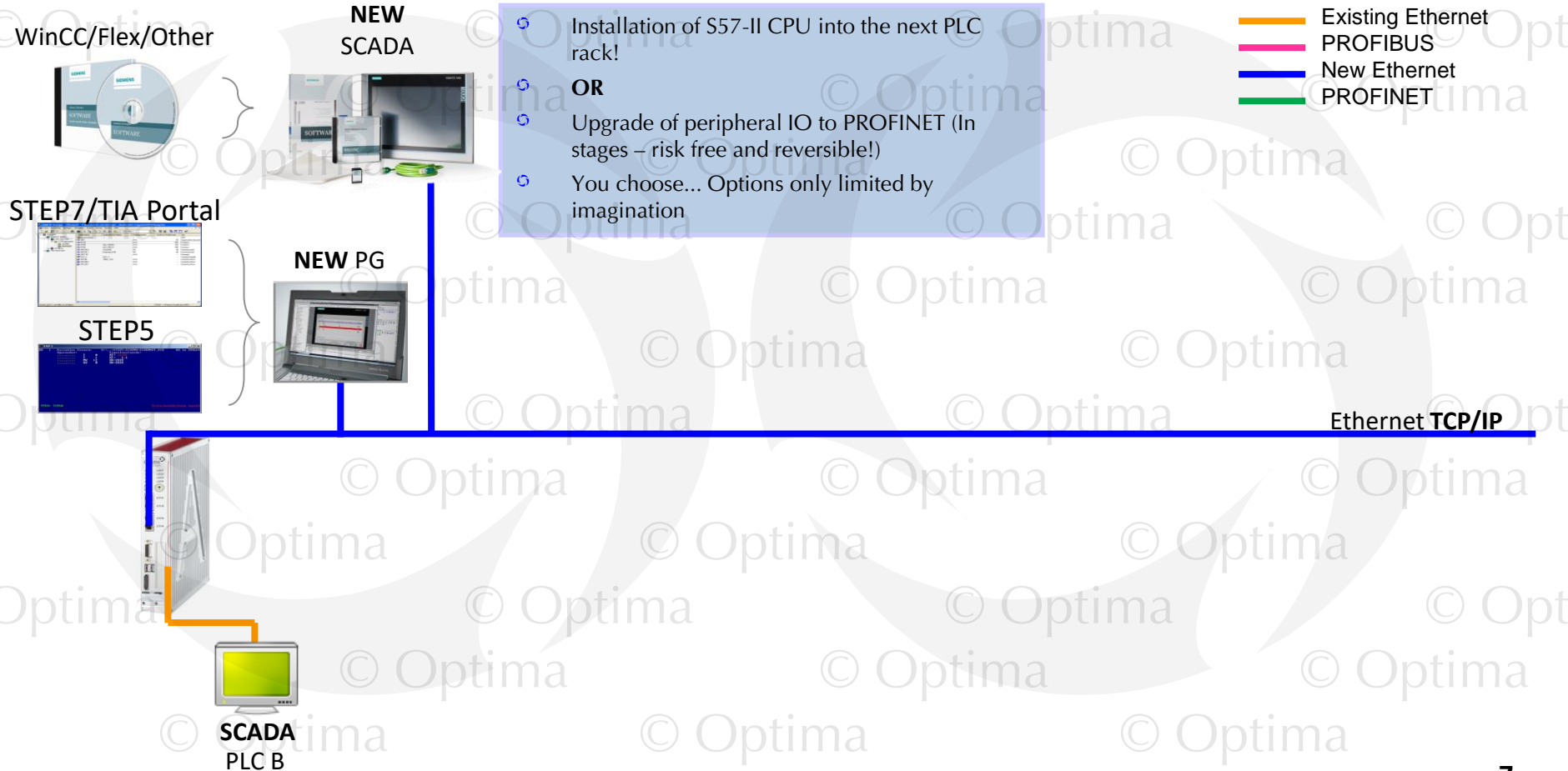
# STEP 2



# STEP 3...



# STEP ... to End





# End

